

X-1549 US
10/791,924

PATENT
Conf. No.: 3818

IN THE UNITED STATES PATENT OFFICE

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| Applicants: | Brian T. Brunn et al. | | |
| Assignee: | Xilinx, Inc. | | |
| Title: | BIT-EDGE ZERO FORCING EQUALIZER | | |
| Serial No.: | 10/791,924 | File Date: | March 2, 2004 |
| Examiner: | Siu M. Lee | Art Unit: | 2611 |
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AMENDMENT IN RESPONSE TO FOURTH OFFICE ACTION

In response to the fourth Office Action mailed from the Patent Office on November 12, 2008, (the "Office Action") reconsideration and allowance of all claims is respectfully requested in view of the amendments and remarks that follow.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.